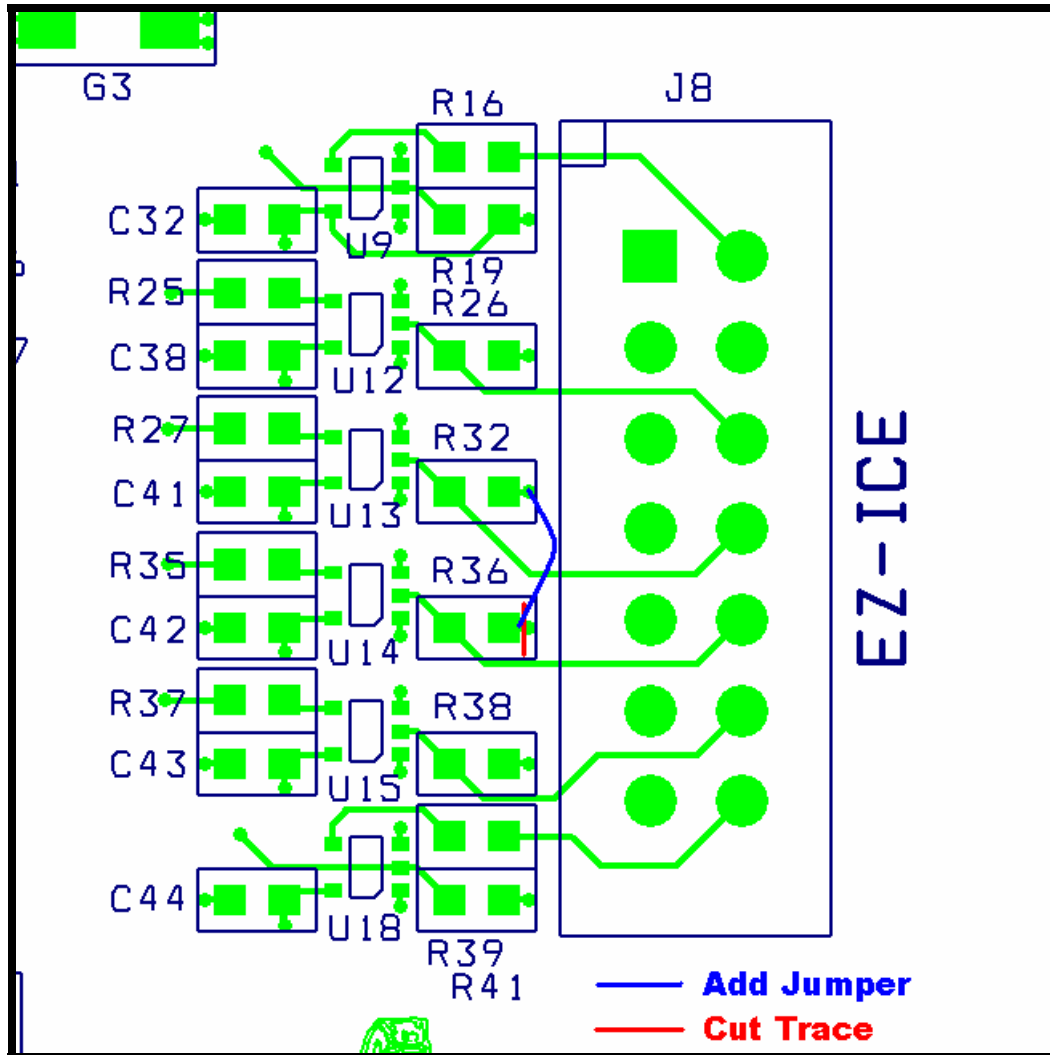


Mod for Debug Board 11683A, R36.

Convert R36 from Pull-down to Pull-up.

This modification seems to have eliminated “Erasing Sector 0” errors during programming.

1. Cut trace from R36 to via that connects to ground (see red mark in drawing for location).
2. Add jumper from same pin of R36 to R32 via. This via connects to 3.3V



This modification will pull BDTRST- high when nothing is connected. This, in turn, will pull the TRST- pin on the ADSP chip of the board-under-test high. This allows JTAG operation. When the test cable is not connected from the debug board to the board-under-test, a resistor pulls the TRST- pin of the ADSP low, preventing the chip from unintentionally entering a test mode.

Test cables that connect to the Xilinx and EZ-Ice connectors on the Debug Board should also be modified. Any existing jumper from pin 2 to Pin 10 of J8 (EZ-Ice connector) should be removed.